

PTO/SB/21 (08-00)

# TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number

09/288,140

Filing Date

April 8, 1999

First Named Inventor

Shunpei YAMAZAKI et al.

Group Art Unit

2871

Examiner Name

M. Ton

Attorney Docket Number

0756-1943

## ENCLOSURES (check all that apply)

☐ Fee Transmittal Form☐ Fee Attached☐ Amendment / Reply☐ After Final☐ Affidavits/declaration(s)☐ Extension of Time Request☐ Express Abandonment Request☐ Information Disclosure Statement☐ Certified Copy of Priority Document(s)☐ Response to Missing Parts/ Incomplete Application☐ Response to Missing Parts under 37 CFR 1.52 or 1.53☐ Assignment Papers (for an Application)☐ Drawing(s)☐ Declaration and Power of Attorney☐ Licensing-related Papers☐ Petition☐ Petition to Convert to a Provisional Application☐ Power of Attorney, Revocation Change of Correspondence Address☐ Terminal Disclaimer☐ Request for Refund☐ CD, Number of CD(s) \_\_\_\_\_

Remarks

☒ The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 50-2280 for the above identified docket number.☐ After Allowance Communication to Group☐ Appeal Communication to Board of Appeals and Interferences☐ Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)☐ Proprietary Information☐ Status Letter☒ Other Enclosures

1. Correction to Previously Submitted IDS and PTO 1449 Form

2.

3.

4.

5.

6.

## SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm  
or  
Individual nameEric J. Robinson, Reg. No. 38,285  
Robinson Intellectual Property Law Office, P.C.  
PMB 955  
21010 Southbank Street  
Potomac Falls, VA 20165

Signature

Date

June 1, 2006

## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

Type or printed name

Adele M. Stamper

Signature

Date

June 1, 2006

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.



Attorney Docket No. 0756-1943

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 09/288,140

Filed: April 8, 1999

For: ELECTRO-OPTICAL DEVICE AND

METHOD FOR DRIVING THE SAME

) Group Art Unit: 2871

) Examiner: M. Ton

) CERTIFICATE OF MAILING

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*Adelle M. Stamper*

CORRECTION TO PREVIOUSLY SUBMITTED INFORMATION DISCLOSURE

STATEMENT

Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

It has come to applicant's attention that several Information Disclosure Statements previously submitted included inadvertent typographical errors or incomplete information.

Specifically, the Information Disclosure Statement filed April 8, 1999 included the following discrepancy:

1) The date of publication was incorrectly listed in the Wolf et al. article as 1989. The correct publication date is 1986.

The Information Disclosure Statement filed March 14, 2000 included the following discrepancy:

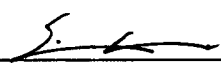
1) U.S. Patent 5,854,879 was cited with an issue date of 12/19/1998. The correct issue date is 12/29/1998.

The Information Disclosure Statement filed December 10, 2001 included the following discrepancy:

1) The volume number was incorrectly listed in the article to Serikawa et al as Vol. 32/2. The correct volume number is Vol. 30/2;

The above inaccuracies are corrected on the 1449 Form attached hereto. The Examiner is requested to initial the attached PTO Form 1449 evidencing consideration of the above references and line through these references on the previously submitted Information Disclosure Statements to ensure that the correct information is printed correctly on any issued patent.

Respectfully submitted,

  
Eric J. Robinson  
Reg. No. 38,285

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Receipt date: 06/05/2006

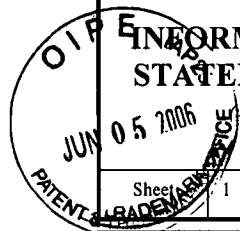
Please type a plus sign (+) inside this box → [+]

PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449A/PTO		<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>		Application Number	09/288,140
		Filing Date	April 8, 1999
		First Named Inventor	Shunpei YAMAZAKI et al.
		Group Art Unit	2871
		Examiner Name	M. Ton
		Attorney Docket Number	0756-1943
Sheet 1	of 1		

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
		5,854,879		Inuzuka et al.	12/29/1998	
				</		

FOREIGN PATENT DOCUMENTS								
Examiner Initials <sup>2</sup>	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Serikawa et al., <i>Low-Temperature Fabrication of High-Mobility Poly-Si TFTs for Large-Area LCD's</i> , Pages 137-141, 1989, Proceedings of the SID, Vol. 30/2.	
		Wolf et al., <i>Silicon Procoessing for the VLSI Era</i> , Lattice Press, Volume 1, 1986, Pages 151-154.	

Examiner Signature	/James Dudek/	Date Considered	09/03/2008
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /JD/